

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Group Art Unit: 2818  
Examiner: DANG, PHUC T

In Re PATENT APPLICATION Of:

**Applicant : Naofumi IWAMOTO**

Appln.No. : To Be Assigned

Div. of Ser. No.: 10/197,529

Filed : herewith

For : METHOD OF MANUFACTURING )  
SEMICONDUCTOR CHIP HAVING A )  
SUPPORTING MEMBER AND )  
SEMICONDUCTOR PACKAGE HAVING )  
THE SEMICONDUCTOR CHIP(amended) )

**PRELIMINARY  
AMENDMENT**

Attorney Ref. : F00ED0440-DIV

November 21, 2003

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

**Sir:**

**Preliminary to examination, please amend the application as follows:**